

Afrin Naz

Education

Ph.D. in Computer Science, University of North Texas, Denton, 2007
Master of Science in Computer Science, Midwestern State University, Wichita Falls, 2000
Master of Science in Microbiology, Dhaka University, Bangladesh, 1995
Bachelor of Science in Biology, Dhaka University, Bangladesh, 1993

Academic experience

West Virginia University Institute of Technology, Assistant Professor, August 2011 – Present, full time.
University of North Texas at Dallas, Lecturer, August 2010 – May 2011, full time.
University of North Texas, Assistant professor, September 2008 – May 2010 full time.,
Drake University, Assistant professor, August 2007 – August 2018, full time.

Certifications or professional registrations

ABET IDEAL Scholar

Current membership in professional organizations

ASEE

Honors and awards

Professional and Curriculum Development 2012, awarded by NASA K-12
Professional and Curriculum Development 2013, awarded by NASA K-12
Computer Science for High School 2013, awarded by Google Inc.
Information Literacy Enhancement project 2013, awarded by WVU
Toyota STEM CAMP 2014, awarded by Toyota

Service activities (within and outside of the institution)

Serving as the accreditation coordinator for the Department of Computer Science and Information System, West Virginia University Institute of Technology
Serving as the Chair of Information Systems Undergraduate Committee for the Department of Computer Science and Information System, West Virginia University Institute of Technology

Serving as reviewers for American Society of Engineering Education, Frontiers in Education, STEM Conference, International Journal of Modeling and Simulation, Workshop on Memory performance dealing with applications, systems and architecture (MEDEA)

Served on the steering committee of 2009 Memory performance dealing with applications, systems and architecture.

Served as Session Chair, Track – Algorithms, *Proceedings of ISCA PDCS-2009*, Louisville, KY.

Served as Editor, Proceedings of International Seminar FOBANA 2008

Presentation and publications

A. Naz, “Best of Two Worlds in a Software Engineering Class, Student Outcomes of Accreditation Board of Engineering and Technology (ABET) and Information Literacy Standards of Association of College and Research Libraries (ACRL)”, Presented at ABET Symposium 2013, Portland, *April 2013*

A. Naz and M. Casto, “Best of Two Worlds in a Software Engineering Class, Student Outcomes of Accreditation Board of Engineering and Technology (ABET) and Information Literacy Standards of Association of College and Research Libraries (ACRL)”, Accepted for the *Proceedings of Frontiers in Education, October 2013*

A. Naz and K. Kavi, “A Smart Cache Designed for Embedded Applications”, *International Journal of Advanced Research in Computer Science, Vol.3, No.1.2012*

A. Naz, O. Adamo, K. Kavi and T. Janjusic. “Improving uniformity of cache access patterns using split data caches”, *Proceedings of ISCA PDCS-2009*, Louisville, KY, Sep. 2009

W. Li, M. Rezaei, K. Kavi, A. Naz and P. Sweany, “Feasibility of Decoupling Memory Management from the Execution Pipeline”, *Journal of Systems Architecture (published by Elsevier), Vol. 53, No. 12, pp 927-936. 2007*

A. Naz, K. Kavi, P. Sweany and W. Li, “Reconfigurable Partitioned Data cache: A Novel Approach for Embedded Systems”, *Proceedings of SAC 2007 22nd ACM Symposium on Applied Computing SPECIAL TRACK on Embedded Systems: Applications, Solutions, and Techniques*, Seoul, Korea, Mar 2007

A. Naz, K. Kavi, P.H. Sweany and W. Li, “Tiny split data caches make big performance impact for embedded applications”, *The Journal of Embedded Computing (Special Issue on Embedded Single-Chip Multi-core Architectures from System Design to Application Support), Vol.2, No.2, pp 207-219, 2006*

A. Naz, K. Kavi, M. Rezaei and W. Li, “Making A Case For Split Data Caches For Embedded Applications”, *March 2006 issue of SIGARCH’s ACM Computer Architecture News (CAN), 2006*